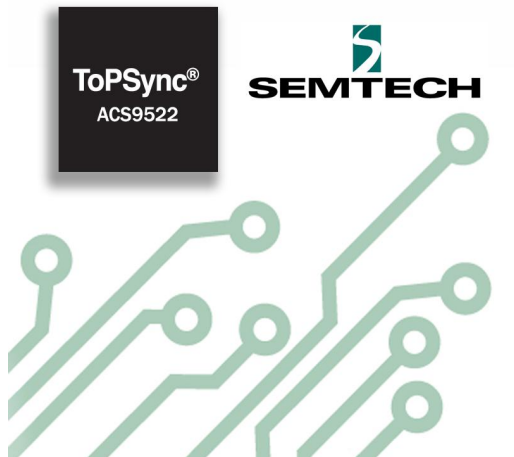


Semtech Extends ToPSync Network Timing Platform with Revolutionary New Single-Chip System

WDD Staff



ACS9522T Saves Cost, Time & Space

[Semtech](#) [1] (Camarillo, CA) has announced the general availability of the ACS9522T, the latest addition to the ToPSync platform that integrates a revolutionary new synchronization system on a chip (SoC). The ACS9522T breaks new ground in the industry by providing a complete single-chip hardware and software synchronization solution for IP-based wireline and wireless applications, with a special emphasis on the requirements of the emerging 4G LTE and small cell market. Features include:

- CPU, memory and six phase-lock loops (PLLs) needed to support IEEE 1588 packet-based synchronization.
- Frequency synchronization via synchronous Ethernet or SONET/SDH.
- Synchronous Ethernet / IEEE 1588 hybrid mode operation.
- Network asymmetry correction.

For more information, visit www.semtech.com [1].

Source URL (retrieved on 02/26/2015 - 7:35pm):

<http://www.wirelessdesignmag.com/product-releases/2013/07/semtech-extends-topsync-network-timing-platform-revolutionary-new-single-chip-system>

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[1] <http://www.semtech.com>