

Integrated Frequency Margining Capability

WDD Staff



[Integrated Device Technology, Inc. \(IDT\)](http://www.IDT.com)

[1] (San Jose, CA) [[NASDAQ: IDTI](http://www.nasdaq.com) [2]] has announced their differential MEMS oscillators with 100 femtosecond (fs) typical phase jitter performance and integrated frequency margining capability. The extremely low-phase jitter and adaptable output frequency of IDT's high-performance oscillators reduce bit error rate (BER) in 10 GbE switches, routers, and other related networking equipment. Features include:

- A differential LVDS/LVPECL output.
- A low-phase jitter of 100 fs @ 1.875 - 20 MHz and sub-300 fs @ 12 kHz - 20 MHz.
- Integrated frequency margining capability, which enables customers to fine-tune the oscillator frequency during operation in the application by up to $\pm 1,000$ ppm.
- Availability in standard 7.0 x 5.0 mm, 5.0 x 3.2 mm and 3.2 x 2.5 mm VFQFPN packages.

For more information visit www.IDT.com [1].

Source URL (retrieved on 01/31/2015 - 2:23am):

<http://www.wirelessdesignmag.com/product-releases/2013/04/integrated-frequency-margining-capability>

Links:

[1] <http://www.IDT.com>

[2] https://www.google.com/finance?q=NASDAQ%3A+IDTI&ei=8NViUcijFsOmQ_QG_NQ

