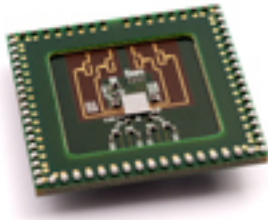


## **Imec & Panasonic Unveil Low-Power Chipset for 60GHz Multi-Gbit Wireless Communication**

WDD Staff



[Imec](#) [1] (San Francisco, CA), in collaboration with [Panasonic Corporation](#) [2] (Japan), has presented at the IEEE International Solid-State Circuits Conference (ISSCC2013) a 60 GHz radio transceiver chipset with low power consumption that delivers high data rates over short distances. Imec drastically boosted the link budget of the system by introducing beamforming into the radio architecture. This multi-Gbit 60GHz chipset paves the way toward small size, low-power, low-cost, high-data rate solutions for battery-operated consumer devices, such as smart phones and tablets. Features include:

- A receiver and a transmitter chip that are based on a direct, conversion architecture combined with an on-chip phased-array architecture.
- 40 nm low-power digital CMOS technology.
- 4 antenna paths.
- Compact mm-wave CMOS layout techniques.
- A 1.1 V power supply.
- A data rate of 2.31 Gbps.
- A 3 dB scan angle range around 120°.
- IEEE802.11ad standard.

For more information visit <http://panasonic.net> [2] and [www.imec.be](http://www.imec.be) [1].

**Source URL (retrieved on 02/01/2015 - 9:23am):**

[http://www.wirelessdesignmag.com/product-releases/2013/02/imec-panasonic-unveil-low-power-chipset-60ghz-multi-gbit-wireless-communication?qt-blogs=0&qt-digital\\_editions=0&qt-most\\_popular=0](http://www.wirelessdesignmag.com/product-releases/2013/02/imec-panasonic-unveil-low-power-chipset-60ghz-multi-gbit-wireless-communication?qt-blogs=0&qt-digital_editions=0&qt-most_popular=0)

**Links:**

# **Imec & Panasonic Unveil Low-Power Chipset for 60GHz Multi-Gbit Wireless**

Published on Wireless Design & Development (<http://www.wirelessdesignmag.com>)

---

[1] <http://www.imec.be>

[2] <http://panasonic.net>