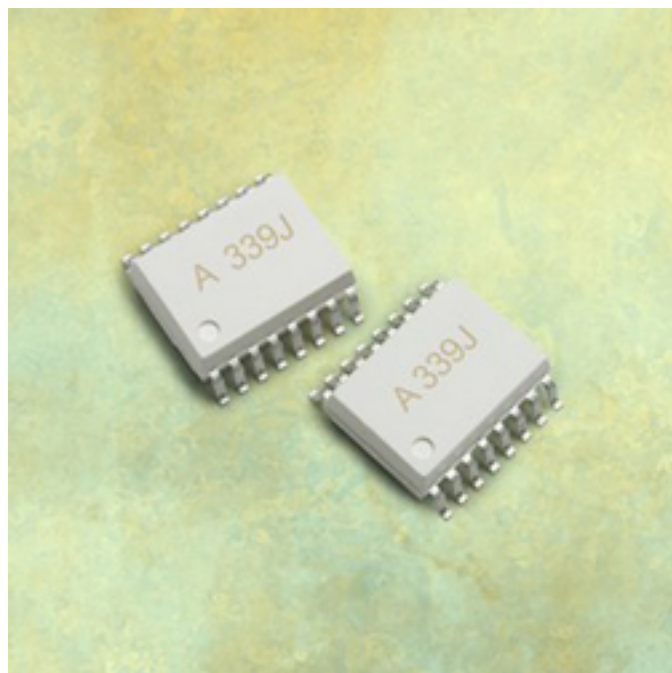


# Avago Technologies' New Gen of Smart Gate Drive Optocoupler

WDD Staff



[Avago Technologies](#) [1] (San Jose, CA)

[[Nasdaq:AVGO](#) [2]] has announced a new highly-integrated smart gate drive optocoupler device, the ACPL-339J. The device is a 1A output current gate drive optocoupler with dual outputs optimized for driving both high- and low-side gates of the MOSFET buffer stage. Additional features include:

- An internal active timing control circuitry preventing cross conduction.
- Availability in an SO16 package.
- VCE desaturation (DESAT) detection.
- Under-voltage lockout (UVLO) detection.
- "Soft" IGBT turn-off, and isolated open collector fault feedback.
- Wide creepage and clearance of 8.3 mm.

For more information visit [www.avagotech.com](http://www.avagotech.com) [1].

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<http://www.wirelessdesignmag.com/product-releases/2013/02/avago-technologies%E2%80%99-new-gen-smart-gate-drive-optocoupler>

### Links:

[1] <http://www.avagotech.com>

[2] <http://www.google.com/finance?q=Nasdaq%3AAVGO&ei=UmwSUcjVL8amr>

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