

16-bit ADC at 250 MSPS

The industry's fastest 16-bit ADC (analog-to-digital converter)--at 250 MSPS (mega samples per second)--was unveiled recently by Analog Devices, Inc. (ADI). The AD9467 16-bit, 250 MSPS ADC operates on 35% less power at 25% higher sampling rate than any other 16-bit data converter, providing a new level of signal processing performance for test and measurement instrumentation, defense electronics, and communications applications where high resolution over a wide bandwidth is needed. The AD9467 delivers resolution and a fast sample rate while simultaneously achieving a high SFDR (spurious-free dynamic range) of up to 100 dBFS and SNR (signal-to-noise ratio) performance of 76.4 dBFS. The device's SFDR of 90 dBFS up to 300 MHz analog input and 60-femtosecond rms (root mean square) jitter helps lower the signal chain bill of materials component count by allowing engineers to increase system performance at higher intermediate frequencies, thereby reducing the number of signal down-conversion stages.

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