First HDTV Platform for the Accelerated Verification of HDMI and DisplayPort Products

Mentor Graphics Corporation announces its high-performance platform to accelerate the verification of HDTV products. This platform enables designers to test their HDTV system, including High-Definition Multimedia Interface (HDMI) and DisplayPort interfaces, by employing real-world HDTV stimulus and analysis tools early in the development cycle – delivering verification for thousands of full-HDTV frames. The platform consists of the Veloce&reg family of hardware-assisted verification products and the iSolve&#153 Multimedia product for HTDV applications, which provides a cost-effective and efficient solution, delivering a dynamic and accurate verification environment. HDMI has become dominant in the connection of millions of High-Definition (HD) devices, including flat-panel HD TVs, PCs, HD camcorders, digital video recorders (DVRs), and video game consoles. With a high-speed interface that delivers sustained bandwidth rates, broader and deeper color depths to allow displays to cover billions of colors, and easier connectivity, HDMI has enjoyed a huge success, with over 800 licensees and over 200 million units sold so far. With the world's largest processor vendors, consumer electronics, and PC OEMs supporting these standards, it is critical that there are solutions available to accurately and quickly verify such interfaces on System-on-Chip (SoC) designs. Mentor Graphics' HDTV platform is used as an essential part of the strategy for verifying HDTV chips, where the verification of a single frame of full-HD video takes millions of clock cycles to run. The high-performance of the HDTV platform makes it feasible to run thousands of full-HD frames, and provides full debug visibility for the user.

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