USB PHY Cores Perform in Consumer Applications

MIPS Technologies, Inc. introduces its 40 nm USB PHY IP core and USB-certified 1.8 V 45 nm USB PHY IP core, enabling developers to integrate USB functionality into their advanced SoCs for a large number of consumer applications. These cores represent a new generation of USB physical layer architectures using 1.8 V, or alternatively 2.5 V, I/O devices to deliver low power consumption for 45 nm and 40 nm SoC designs. Low power, along with a compact, silicon-saving design, makes these IP cores suitable for mobile applications. Advanced programmability allows developers to fine-tune the analog parameters of their systems for maximum performance results in silicon.