

## **Automotive Device Family Provides System-on-Chip Functionality**

Lattice Semiconductor Corporation has announced automotive versions of its non-volatile, 90 nm Flash-based LatticeXP2™ FPGAs, which are qualified to meet the certification requirements of the AEC-Q100 standard. The FPGA family includes three members, with capacities from 5K to 17K 4-input Look Up Tables (LUTs). Embedded Block RAM is provided with up to 276 Kbits on-chip in 18 Kbit dual-port blocks. For small scratch pad memories, LUTs also can be converted into small, distributed memory blocks. To support DSP applications, up to 5 sysDSP™ blocks provide hardwired, high-performance pipelined multiply and accumulate functions. Lattice designed the LatticeXP2 family to use a 1.2V core voltage for low power consumption.

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