

## Devices Deliver Ultra-low Phase Noise



Peregrine Semiconductor's UltraCMOS<sup>®</sup> PE97022 and PE97042 phase locked loop (PLL) frequency synthesizers are in production and available for satellite payload designs. The new pair of 3.5 GHz Integer-N PLLs provide an ultra-low normalized phase noise of  $\pm 16$  dBc/Hz, and the two PLLs feature programmable counters, a phase comparator and +10/11 Dual Modulus Prescaler, allowing for backward compatibility to previous generation PLLs. The PE97042 offers direct or serial programming modes, while the PE97022 offers an additional parallel mode option. The UltraCMOS silicon-on-sapphire based devices are fully space qualified and immune to Single Event Latch-up (SEL). A superior resistance to Single Event Upset (SEU) of  $<10^{-9}$  errors per bit-day and tolerance to total dose radiation of up to 100 Krads (Si) make these devices suited for the demanding rigors of space applications.

[www.psemi.com](http://www.psemi.com)

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