

Third Generation Non-volatile FPGAs



Lattice Semiconductor Corporation announces the availability of its third generation non-volatile FPGAs, the LatticeXP2 family. With enhanced capabilities, the LatticeXP2 family doubles maximum logic capacity to 40K Look Up Tables (LUTs), improves performance 25% and adds dedicated DSP blocks, all while reducing the price per function by up to 50%. Power consumption has also been optimized on the 1.2 V process technology, reducing static power usage by 33%. Designed using the advanced non-volatile FPGA technology, a 90 nm embedded Flash process co-developed with partner Fujitsu, the LatticeXP2 devices provide the “instant-on” and reduced footprint benefits of earlier non-volatile devices, while also enhancing design security, RAM back-up and.

www.latticesemi.com 503-268-8000

Source URL (retrieved on 01/26/2015 - 9:08pm):

<http://www.wirelessdesignmag.com/product-releases/2007/07/third-generation-non-volatile-fpgas>