

Second Generation Power Management Devices

Lattice Semiconductor introduces its second generation Power Manager II devices along with details of the first device available, the ispPAC[®] POWR1220AT8. The Power Manager II family is a functional superset of the company's earlier ispPAC Power Manager mixed-signal devices that provide a complete power management product for printed circuit boards (PCBs) through an optimized set of programmable digital and analog functions. Analog features such as input comparator thresholds and digital functions such as supply control sequences are programmed into non-volatile E²CMOS[®] elements on the devices using an IEEE1149.1 protocol. The Power Manager II devices add power supply margining and trimming to first generation device features such as power supply voltage sequencing and monitoring. The POWR1220AT8 device integrates a 48 Macrocell ruggedized CPLD, dual precision voltage monitoring comparators with an accuracy of 0.5%, a 10-bit Analog to Digital Converter (ADC) for voltage measurements, and eight 8-bit Digital to Analog Converters (DAC) for trimming power supplies.

Lattice Semiconductor

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