MATLAB Test and Measurement Applications for Communications

This seminar will cover the design of a communications subsystem using MATLAB(R) and Simulink(R), implementation on an FPGA, and subsystem testing and verification by taking real world measurements and comparing modeled behavior with actual behavior.

Engineers from The MathWorks will demonstrate tools that support the communications and electronics design flow, including prototyping designs, modeling system behavior, design verification and validation, and code generation. Attendees will learn the benefits of designing and validating the architecture and behavior of communications devices early in the design process, resulting in a reduction of overall development time, cost, and risk.

To register for these or other seminars sponsored by The MathWorks, visit http://www.mathworks.com/seminars/semalert.