Design Techniques Using SystemView

Design Techniques Using SystemView is intended to familiarize engineers with SystemView capabilities and tools. The course provides practical, hands-on training in system design using SystemView. This course is essential for engineers wishing to gain an understanding of the practical use of simulation-based analysis and design software as part of their development process. It is structured as a three-day course consisting of lectures and hands-on lab exercises.
Day one is an introduction to SystemView that provides instruction on basic operation, including useful shortcuts and techniques that will increase your productivity. Days two and three provide theory and hands-on instruction in various design techniques.

Day One: Introduction to SystemView

&#149 How the simulation engine works
&#149 Setting the time base
&#149 Setting parameters and tokens
&#149 The SystemView libraries and their functions
&#149 Sources and sinks
&#149 Metasystem creation
&#149 Sampling and decimating
&#149 Duplicating and connecting tokens
&#149 Global parameter links
&#149 Analysis window and block processing
&#149 Importing and exporting data
&#149 Scheduler use to control states of systems
&#149 Debugging with the Dynamic System Probe

Days Two and Three: Design Techniques

&#149 Communications System Design
&#149 QPSK modulation
&#149 BER analysis
&#149 Synchronization and timing
&#149 Baseband pulse shaping

RF System Design

&#149 noise figure
&#149 mixers
&#149 amplifiers
&#149 S-Parameters

DSP System Design

&#149 Bit-true design
&#149 Using the DSP flags
&#149 Arithmetic modes

General System Design

&#149 FFT Analysis
&#149 Implicit delays
&#149 Filter design
&#149 Baseband simulation techniques

&#149 Multi-rate simulation

Special Topics

&#149 User Code design
&#149 APG Automatic Program Generation
&#149 Real Time Co-Simulation with TI DSK
&#149 802.11 a/b
&#149 3G Design
&#149 HDL solution, System design, RTL, test and verify (NEW)

Who Should Attend

Engineers, technicians and managers involved in digital, analog, or mixed-mode system design.

Duration 3 days

Date Refer to enrollment form for scheduled course dates
Time 9:00am – 4:30pm
Location ExecuTrain of Thousand Oaks
223 E. Thousand Oaks Blvd., Suite 401
Thousand Oaks, CA 91360
Price $1475
Terms Payment in full must be received no later than three weeks before the course date.
Class size is limited to 12 students. Space is available on a first come, first served basis. ELANIX reserves the right to cancel classes and will endeavor to notify students of cancellations at least one week before the scheduled start of class.

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