

Accelerator Chip



The JA108 Java accelerator chip targets mobile wireless applications, such as 2G/2.5G/3G phones, and it improves Java software execution while extending battery life. The JA108 is the first in a series of product offerings under Nazomi's KChip¹⁵³ product line. The JA108 offloads the microprocessor by taking over the task of executing Java bytecode instructions quickly and power-efficiently and thus speeds up Java software execution by 15x to 60x, depending on the application. It accomplishes this without increasing system clock frequencies, requiring additional memory, new tools or causing any new porting efforts. The chip, which interfaces like a standard SRAM device, is a stand-alone Java Accelerator that can be easily integrated into new and existing designs on existing memory buses. The chip is available either in a 10 × 10 mm 128 ball BGA .8 mil pitch, or a 7 × 7 mm 128 ball BGA .5 mil pitch configuration. The JA108 works with any baseband processor, chipset, system on chip (SoC), or microprocessor and is transparent to existing designs and legacy operating systems.

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