

Clock Oscillators to 350 MHz



To meet the demand for a cost-effective reference clock with improved performance and reliability for Infiniband at 10 Gigabit Ethernet, SaRonix introduces the SDS3811 – a high frequency LVDS clock oscillator. Next-generation networking can benefit from the lower power and higher frequencies available from Low Voltage Differential Signaling (LVDS) with the new SDS3811. Using AT-cut bulk acoustic wave instead of surface acoustic wave (SAW) technology, the SDS3811 provides a five-fold improvement in frequency stability, resulting in a stable oscillator at high frequencies, with overall stabilities of ± 17720 ppm over 0°C to $+70^{\circ}\text{C}$ and ± 17732 ppm over -50°C to $+85^{\circ}\text{C}$.

This leading edge technology is contained in a surface-mount 5 mm \times 7 mm 1.8 mm, seam-sealed miniature ceramic package with six gold-plated contact pads. With the output enable function, the complementary outputs can be Tri-Stated to facilitate testing or combining multiple clocks.

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