

Family of Low-Power Synchronous DRAMs



Micron Technology, Inc., today introduced the first devices in its new BAT-RAM[®] family of low-power synchronous DRAMs (SDRAMs). Micron's new BAT-RAMs are designed to consume less power than standard SDRAMs, and extend the battery life in many mobile and wireless applications. Micron is currently sampling 2.5 and 3.3 Volt 2 Meg x 32 64Mb BAT-RAM devices and will introduce next generation devices later this year. Micron's new BAT-RAM family cost-effectively provides the densities, organizations, and speeds critical for the battery-powered world. Micron's BAT-RAM devices include a new feature, temperature compensated self refresh (TCSR). When enabled and programmed by the system, this feature allows the part to adjust refresh rate and power consumption based on temperature. At normal room temperatures, this feature allows the devices to consume even less power. JEDEC is currently defining standards for low-power DRAMs.

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