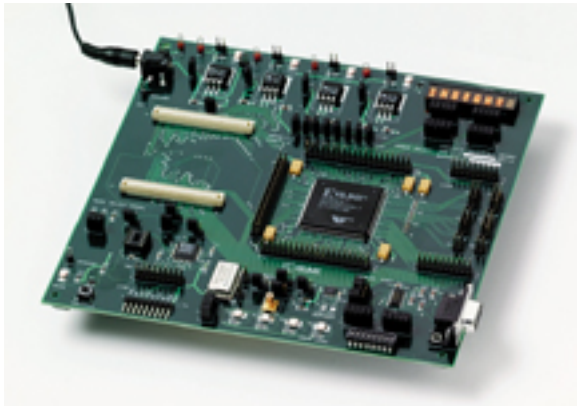


## Development Kit For FPGA Designers



Insight Electronics now offers a Virtex-E

Development Kit that helps FPGA designers shorten the development cycle and meet their time-to-market requirements. The new kit provides a platform for prototyping and verifying designs based on Xilinx's Virtex-E family of FPGAs. The development kit includes a Virtex-E reference board based on either a 600K-gate or 300K-gate Virtex-E FPGA. The board contains power supply regulation, user configurable I/O bank voltages, an 8-character LED display, an RS-232 port, user switches and LEDs, an LVDS test port, expansion connectors, and an in-system programmable XC18V00 series PROM. These circuits are designed to provide necessary supports functions without diminishing user flexibility.

The board can be turned on, connected to a JTAG or MultiLINX cable, and configured with an FPGA bit stream quickly. Reference designs in VHDL and Verilog are also available as examples, along with the respective bit stream files.

The Xilinx Virtex-E FPGA and supporting board circuits assist FPGA designers in prototyping high-performance memory and I/O interfaces, low-voltage differential signaling (LVDS), and various user application-specific functions by way of an option module.

The Virtex-E device's performance and density, the on-board ISP PROM's in-system programmability, and the complete high-performance LVDS support combine with Insight Reference Design Center's library of pre-configured designs to make the new development kit an ideal solution for FPGA and system designers who need a quick and flexible, prototyping platform.

In addition, Insight's Virtex-E platform is well suited for evaluating two types of embedded processor implementations &#151; companion processors and soft cores.

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