

# Cavium Introduces TurboDPI II for OCTEON Processors

Cavium, Inc.

### **Takes deep packet inspection (DPI) to the next level.**

San Jose, CA—[Cavium, Inc.](#) [1] ([NASDAQ: CAVM](#) [2]) has introduced TurboDPI II, the next generation of its production-ready TurboDPI family of software, that now incorporates up to nine major DPI functional modules integrated into a holistic toolkit that have been optimized for OCTEON II processors.

TurboDPI II's Uniscan technology ensures that data is only scanned once by the OCTEON's built-in hardware HFA DPI engines, and then the results are passed to the appropriate module.

TurboDPI II enables market-leading DPI performance and speeds up time to market for Enterprise Cloud, Data Center and 3G/4G LTE Wireless equipment manufacturers. TurboDPI II supports all of Cavium's OCTEON II family of processors, which is the industry's leading embedded multi-core processor line designed into enterprise, data center and service provider equipment including routers, switches, appliances, 3G/4G wireless base stations, RNCs, xGSNs, evolved packet core, services gateways, DPI equipment, storage switches and intelligent server adapters.

Time to market and the provision of rich services are top of mind for providers of Next-Generation Networks as they strive to handle the explosive increase in traffic because of the fast adoption of cloud technology and mobile broadband, as well as increased exchange of multimedia and video rich content.

Higher traffic coupled with the need for intelligent application-aware and secure processing not only requires advanced multi-core processors and dedicated hardware acceleration, but also efficient, top class software to go along with it.

TurboDPI II is designed as a modular system with nine optional DPI modules, which include support for Application Recognition, Security and Performance monitoring.

"In the last two years, our TurboDPI product line has been taken up by a number of Tier 1 players in the 3G/4G infrastructure and Enterprise Markets", says YJ Kim, Vice President, Infrastructure Processor Division, Cavium. "Now with nine DPI modules, TurboDPI II provides new functionality for the Cloud and Data Center markets and will help service the ever growing demand for finer grained network, application visibility and security."

TurboDPI II has already been ported and deployed on many OCTEON Platforms including Cavium Evaluation Boards (EVBs), Cavium small form factor (SFF) evaluation boards, Intelligent Network Interface Cards (NICs), AMC boards and high

## **Cavium Introduces TurboDPI II for OCTEON Processors**

Published on Wireless Design & Development (<http://www.wirelessdesignmag.com>)

---

performance ATCA blades capable of up to 80Gbps of Application Recognition that utilize dual 32 core Cavium OCTEON CN6880 processors. Major vendors including Advantech, Emerson, Kontron and Radisys all offer equipment that supports TurboDPI II.

Cavium also offers Data Plane Acceleration Toolkits that utilize the features in our SDKs and the extensive range of OCTEON hardware acceleration capabilities to speed packet handling, TCP, IP, SSL and IPSEC processing.

Cavium offers a wide range of engineering services to assist customers with their projects and speed their time to market. Cavium's Architecture Plus service is designed to help customers integrate TurboDPI or other Cavium Software into their designs. Our Development Plus service provides additional development services to supplement the customers software engineering team to help execute on a particular architectural design.

For more information visit <http://www.cavium.com> [1].

### **Source URL (retrieved on 10/02/2014 - 1:09pm):**

[http://www.wirelessdesignmag.com/news/2013/02/cavium-introduces-turbodpi-ii-octeon-processors?qt-blogs=0&qt-most\\_popular=0](http://www.wirelessdesignmag.com/news/2013/02/cavium-introduces-turbodpi-ii-octeon-processors?qt-blogs=0&qt-most_popular=0)

### **Links:**

[1] <http://www.cavium.com>

[2] <http://www.google.com/finance?q=NASDAQ%3A+CAVM&ei=Xk8IUaiQEoSxRQHmzwe>