First Converter to Incorporate New Embedded DisplayPort v1.2 Features

SAN JOSE, Calif. -- (BUSINESS WIRE)-- Parade Technologies Ltd. announced two new devices that will help manufacturers bridge the transition from LVDS to DisplayPort™ interfaces in notebook and all-in-one computers. Parade's new PS8615 DisplayPort-to-LVDS Converter IC converts single or dual link DisplayPort inputs into single or dual link LVDS outputs, supporting display resolutions up to 1920x1200 (at 60 Hz). The new PS8612 converts a single lane DisplayPort signal to LVDS, which supports display resolutions up to 1680x1050 (at 60 Hz). "Many of the newest notebook chipsets have already abandoned LVDS outputs, and leading CPU vendors have indicated that they will completely transition to DisplayPort by 2013," explains Jimmy Chiu, VP of Marketing at Parade Technologies. "Meanwhile there continues to be considerable manufacturing capacity for display panels that use LVDS inputs. Our two new conversion devices permit OEMs to mix-and-match computer chipsets and display panels to optimize price and performance in individual systems." Both the PS8612 and PS8615 require only a single 3.3v supply. The devices incorporate an on-chip voltage-switching regulator to power 1.2v core circuitry, eliminating the need for external regulators and minimizing power consumption. Power consumption is less than 200mW in typical applications. Both devices also incorporate Parade's crystal-free technology, eliminating the need for an external crystal or clock reference. The PS8612 and PS 8615 are both fully compatible with VESA DisplayPort specification 1.1a, and the newly released Embedded DisplayPort specification version 1.2. Both devices support DisplayPort link rates up to 2.7Gbps and LVDS clock rates up to 135 MHz. The devices support DisplayPort or Embedded DisplayPort (eDP™) with no-link training, fast-link training and full-link training. As a key contributor to the development of eDP specification version 1.2, Parade is the first to adopt eDP v1.2 backlight control into a DisplayPort-to-LVDS converter. This feature allows the PS8612/15 to directly generate the backlight control PWM (pulse width modulated) signal under control of the GPU, using the eDP v1.2 AUX channel control commands. This simplifies interconnect between the GPU and video interface.

Source URL (retrieved on *03/09/2014 - 4:33pm*):
http://www.wirelessdesignmag.com/news/2010/11/first-converter-incorporate-new-embedded-displayport-v12-features